

JAN 03 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: DeLoach et al. Art Unit: 2822
Serial No.: 10/703,387 Examiner: Thomas, T.
Filing Date: 11/06/2003 Docket No.: TI-35862
Customer No.: 23494 Conf. No.: 9996

Title: FORMING A TRENCH TO DEFINE ONE OR MORE ISOLATION
REGIONS IN A SEMICONDUCTOR STRUCTURE

REQUEST FOR EXTENSION OF TIME

Commissioner of Patents
PO Box 1450
Alexandria, VA 22313-1450

Dear Sir:

CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that the following papers are being transmitted by
facsimile to the U.S. Patent and Trademark Office at 703-872-9308 on
the date shown below:


Jacqueline J. Garner, Reg. No. 36,144January 3, 2005
Date

Pursuant to 37 CFR 1.136(a), Applicant(s) respectfully petitions the Commissioner
for an extension of the shortened statutory period for response in the above-identified
Application.

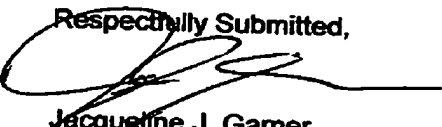
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Texas Instruments Incorporated
PO Box 655474, M/S 3999
Dallas, TX 75265
(214) 532-9348
Fax: (972) 917-4418

Respectfully Submitted,


Jacqueline J. Garner
Reg. No. 36,144

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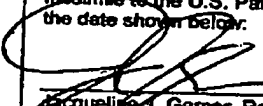
REQUEST FOR EXTENSION OF TIME

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Pursuant to 37 CFR 1.136(a), Applicant(s) respectfully petitions the Commissioner
for an extension of the shortened statutory period for response in the above-identified
Application.

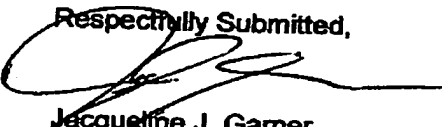
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